i960® Processor Targeted Ada Development Systems
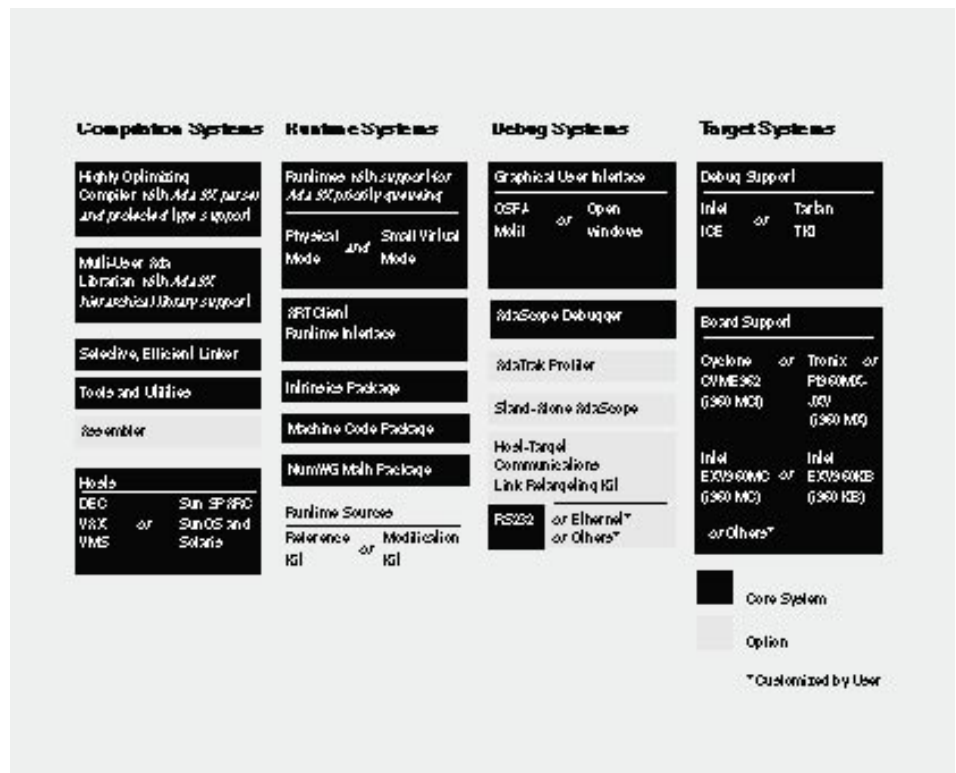
- Complete, Validated Ada Cross Development Systems
- Ada 95 Features Including Protected Type, Child Libraries, Priority Queing and a Full Ada 95 Parser
- Support For the i960® KB, i960 MC and i960 MX Processors
- Hosted on DEC VAX/VMS and Sun SPARC Workstations Running SunOS or Solaris
- Five Levels of Optimization Possible
- Small Virtual Mode Runtime For i960 MC and i960 MX Processors Take Advantage of Multiple Protected Application Capabilities
- Physical Mode Runtime For i960 KB, i960 MC and i960 MX Processors Maximizes Runtime Efficiency For a Single Application
- Run-Time Client Package Gives Ada Applications Direct Access to Ada Tasking Operations
- Source- and Machine-Level Symbolic Debugger
- Optional Dynamic Execution Profiler

The Tartan Ada Development System includes a highly optimizing compiler and a tool set for designing, debugging and maintaining real-time embedded applications.

Tools provided include a full-featured multi-user Ada program librarian, the Small Virtual Mode and Physical Mode runtime systems, a flexible linker that precisely controls memory layout and eliminates unused code sections, AdaScope debugger, static analyzer, advanced listing tools, object file librarian and file format conversion utilities. Standard packages provided include predefined Ada packages, run-time client package, a machine code package for generation of assembly instructions, intrinsics package for access to hardware instructions and Tartan math package that meets the SIGAda NumWG specification.

AdaScope provides full tasking and exception support, debugging of generics and inlined code at the Ada source level, a customizable user interface, point-and-click convenience and the ability to add Ada statements using a built-in Ada subset interpreter (code patching).

PROCESSORS SUPPORTED:

The i960 MC processor system supports the Cyclone CVME962 VME-bus board and the Intel EXV-960 MC board. The i960 MX processor system supports the Engenuity Systems PI960MX-JXV JIAWG Execution Vehicle. The i960 KB processor system supports the Intel EXNV-960KMB board. The Intel ICE-960 MC25 in-circuit emulator is also supported. A Board Retargeting Kit allows the debug kernel to be customized to additional boards.

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